IDEAL RECTIFIER BRIDGE CONVERTING THE HARVESTED ENERGY OF VIBRATIONS INTO ELECTRIC ENERGY TO POWER AN MR DAMPER

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Abstract: The newly developed ideal rectifier bridge equipped with four N-type MOSFETs and two rail-to-rail operational amplifiers is a part of a typical energy harvesting conditioning circuit responsible for the rectification stage in the system of converting the energy harvested from vibrations into electrical energy to power the MR damper. The only energy loss in the bridge is caused by the voltage loss in transistors' channels. The first sections of the work summarises the structural design of the bridge, the simulation procedure under the RL load and by sine voltage inputs with predetermined frequency and amplitude range, and benchmarks the results against those obtained for the conventional bridge based on Schottky diodes. In the second section, the PCB prototype of the bridge is analysed, and measurement data are compiled. The third section reports on the laboratory testing of the developed bridge converting the harvested energy in an MR damper-based vibration reduction system.

Key words: Energy Harvesting, Rectifier Bridge, Voltage, Current, MR Damper

1. INTRODUCTION

Energy harvesting vibration reduction systems with MR dampers have attracted a great deal of attention recently. Their main advantage is that they are able to effectively reduce the vibration amplitude of the sprung mass at near-resonance freguency using the energy harvested from vibrations. Such systems provided in e.g. automobiles and railway vehicles (Sung and Choi 2008, Wang and Liao 2009a, Wang and Liao 2009b) utilize ambient vibrations to excite the harvester harmonically, thereby producing an alternating voltage or AC power. The AC power has to be conditioned before it can be effectively used with any electronics or storage elements requiring DC power (Safei et al. 2019). A typical energy harvesting conditioning circuit has two major components: the rectification (AC/DC conversion) and regulation stage (DC/DC conversion). The simplest energy harvesting circuit is the full-wave rectifier (converting AC signals into all positive voltage) integrated with a smoothing capacitor (converting the signals to DC).

As demonstrated in Sapiński (2011), the energy harvesting vibration reduction system with an MR damper has three separate components: an electromagnetic harvester, an MR damper and an energy conditioning circuitry. Similar three components can be distinguished in the energy harvesting MR damper reported in Sapiński (2014). It appears that in both cases, the system is able to adjust itself to structural vibrations, however, in the limited range of the harvester output voltages. The constraint imposed by the limited range of the harvester output voltage is the consequence of the predetermined range of MR damper piston velocity (amplitude and frequency of vibrations). Assuming that the har-

vester and MR damper parameters are known beforehand, of particular interest is the AC/DC conversion of the harvester output voltage such that the power loss should be as low as possible.

In their former studies, the authors used the rectifier bridge incorporating of the Graetz bridge based on Schottky diodes. Those diodes are widely used in automotive power systems as protection from the effects of reverse battery conditions and of other automotive electrical transients.

When analysing literature on the subject, the authors' attention has been drawn to original solutions concerning rectifier bridges, which are described in many research reports. For example, Balato et al. (2017) demonstrated an H-bridge Graetz rectifier with a smoothing capacitor to convert AC current generated in resonant electromagnetic vibration harvester into DC current supplying resistance load. Snamina and Orkisz (2014) proposed an H-bridge Graetz rectifier, enabling the accumulation of energy recovered from vibrations in 2DOF mechanical system in largecapacity capacitors. Grzybek and Micek (2017) described an H-bridge Graetz that was applied in the mechanical system with a self-powered vibration sensor, including piezoelectric material. It should be noted that the rectifier bridges presented in those works concern silicon diodes. The forward voltage drop of the Schottky diodes, however, results in significant power loss at high currents and enhances the need for thermal management using heatsinks and a larger PCB space. For this reason, the authors undertook to develop a new rectifier bridge to eliminate the aforementioned drawbacks so that it should be applicable in MR damper-based vibration reduction systems and recommended the use of N-type MOSFETs in place of Schottky diodes (Selevaraj 2019).



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It is worthwhile to mention the solution by Maiorca et al. (2013) proposed to use of a diode-less mechanical H-bridge rectifier, in which there are no voltage drops in the forward direction. However, this solution is problematic due to the mechanical strength of the rectifier elements and the breakdown that may occur at the time of switching. The solution most similar to the one discussed in this work was proposed by Chytil (2014) where the ideal rectifier bridge was based on a commercial chip that controls four transistors. Also, this solution is not possible to use in the application described in Sapiński (2011) due to the too high voltage (9 V) required for the proper operation of the H-bridge rectifier.

The present paper is organized as follows. Section 2 summarises the design structure and operating principle of the ideal rectifier bridge. Section 3 benchmarks the simulation results of the bridge under the RL load and by sine voltage inputs with predetermined frequency and amplitude range against those obtained for the Graetz bridge based on Schottky diodes. Section 4 describes PCB of the bridge. Section 5 collates the measurement data obtained for the two bridge types and discusses the experimental results in relation to simulation data. Section 6 is focused on the laboratory testing of the developed ideal bridge employed for converting the energy harvested from vibrations into electric energy used to power the MR damper. Finally, the main conclusions are presented in Section 7.

2. DESIGN STRUCTURE AND OPERATING PRINCIPLE

The objectives underlying the bridge design are closely associated with parameters of the prototype electromagnetic harvester device (Sapiński 2010) and of the Lord RD-8040-1 MR damper (Lord Corporation 2020) and their application in vibration reduction systems with energy harvesting capability. As shown in Fig. 1, the bridge is positioned between the harvester coil and the control coil of the MR damper. The input to the bridge is a voltage signal induced in the harvester coil (proportional to vibration velocity), the output signal being the rectified voltage, supplying the control coil of the MR damper.



Fig. 1. Schematic diagram of the harvester - ideal rectifier bridge - MR damper system

The underlying assumptions are as follows :

- the maximal amplitude and frequency of voltage induced in the harvester coil should be A=10 V and f=10 Hz,
- resistance and inductance of RD-8040-1 damper coil should be R_d =5Ω and L_d =100 mH,
- the bridge performs with the efficiency approaching 95% as long as the amplitude of voltage generated by the harvester is of the order of more than 10 mV,
- the electric energy required to power the bridge components comes either from the harvester (parasite mode) or from an external local storage device (a Li-ion cell or a battery of capacitors),
- in the conditions of no power supply to the bridge components, the bridge begins to operate as a conventional bridge based on Schottky diodes (with a decidedly lower efficiency).

It is worthwhile to mention that the bridge can be adapted to operate at higher frequency and amplitude of voltage induced in the harvester coil than the levels assumed in the present study.

The diagram of a newly developed bridge (Fig. 2) features three major blocks: the supply block (SB), the control block (CB) and the rectifier block (RB). The BOOST (U1) DC/DC converter in the SB block enhances the input voltage u_{IN} according to the actual position of the jumper JP1 (available positions: 1, 2, 3). In its position 1, converter U1 is supplied with rectified voltage u_{DC} , whilst the bridge operates in the parasite mode $u_{IN} = u_{DC}$.

In position 2, converter U1 is supplied with voltage from an external source u_{EXT} – i.e. the bridge operates in the external supply mode. Converter U1 elevates the voltage u_{IN} to u_{OUT} =20 V with respect to the GND reference, when $u_{IN} \ge$ 3V. In position 3, converter U1 is disconnected from the power supply – the bridge operates in the passive mode acting as a conventional Graetz-type rectifying bridge based on Schottky diodes D1-D4.



Fig. 2. Schematic diagram of the ideal rectifier bridge

Converter U1 is requisite to ensure the adequate control of transistors Q1-Q4 whose operation in the linear range is undesirable. Sources of transistors Q3 and Q4 are connected to points of various potential, the potential difference yielding voltage u_h (Fig. 2). Consequently, voltage u_{OUT} must be larger than the sum of u_h and the minimal gate-source threshold voltage U_{GSmin} of transistors Q3 and Q4. The block SB is provided with fault and overload protection, as well as u_{EXT} reverse polarity protection and electrostatic discharge protection (ESD).

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Block CB incorporates two operational rail-to-rail amplifiers U2 and U3 operated in the differential configuration with the amplification 10,000 [V/V]. Amplifiers U2 and U3 use the complete span between the negative and positive supply rail for signal conditioning at the input and output. Their function is to amplify the drain-source voltage in transistors Q3 and Q4. The duty cycle of the bridge involves two phases: 1 and 2. At the onset of phase 1, transistors Q1-Q4 remain closed (in off-position), whilst voltage u_h produces the current flow through diodes D2 and D4. The potential difference between the drain in transistor Q4 and its source is amplified by op-amp U2. The gatesource voltage U_{GS} of transistors Q2 and Q4 is larger than the voltage required for their full opening (UGS>UGSmin), resulting in immediate current flow i_h through transistors Q2 and Q4. Current i_{DC} flows through the two-terminal network R_dL_d and returns to the harvester coil. The only power loss in the ideal bridge circuit is due to a voltage drop on open channel/drain-source on-state resistances in transistors Q2 and Q4. Throughout phase 1, transistors Q1 and Q3 remain closed, whilst their gate potential is equal to that of their sources. Phase 2, in which transistors Q1 and Q3 are operated and controlled via amplifier U3, proceeds in a similar manner as phase 1.

Block RB is comprised of four N-type MOSFETs (the nominal open channel resistance being 5 m Ω) connected in the H-bridge configuration. Schottky diodes D1-D4 connected in parallel with transistors Q1-Q4 enable the passive mode operation (with no power supply).

3. SIMULATION PROCEDURE

Two variants of bridge systems were investigated in the simulation procedure: a conventional rectifier (B1) based on Schottky diodes and an ideal rectifier (B2) incorporating N-type MOSFETs and two rail-to-rail operational amplifiers. The simulation procedure use the LTSpice environment (Analog Devices, 2020). The purpose of the procedure was to determine the extent of power loss in the form of heat released on diodes D1-D4 in bridge B1 and on transistors Q1-Q4 in bridge B2. Key features that have implications for the simulation procedures are as follows:

- the diodes D1-D4 are the type RBR2MM60C (Rohm, 2020), transistors Q1-Q4 are IRFH5007 type (Infineon, 2020);
- the output in each bridge is connected with the control coil of the MR damper modelled as a two-terminal network R_dL_d (resistance-inductance load), without taking into account the processes in the magnetic circuit;
- the harvester coil is an ideal source of sinusoidally varied voltage with amplitude A and frequency f (its resistance and internal inductance being neglected);
- the capacitor smoothing ripples of voltage u_{DC} in the bridge outlet is neglected;
- in the case of bridge B2, the power supply comes from the battery u_{EXT}=3.7 V in the SB block.

Simulation results are illustrated in Figs. 3-6. Apparently, the voltage drop ($|u_h|$ - u_{DC}) across bridge B1 is about 0.7 V, which is equal to 2x forward voltage drop U_F in diodes D1-D4 for the instantaneous current level i_{DC} (Fig. 3a, b). When A=2 V, the maximal voltage u_{DC} becomes 1.3 V (Fig. 3a). Negative voltage values u_{DC} (Fig. 3b) registered when voltage u_{IN} falls in the range ($-U_F$, U_F) are attributable to resistance-inductance loading of bridge output (the two-terminal system R_dL_d). Voltage drop across bridge B2 is close to zero, whilst $u_{DC} \approx |u_h|$. Power loss in bridge circuit B2 is associated with voltage drop across the drain-source resistances in transistors Q1-Q3 or Q2-

Q4. Voltage drop across the transistors is proportional to their drainsource on state resistance and to current i_{DC} . The values of voltage u_{DC} are always positive – block CB with transistors Q1-Q4 is able to effectively dissipate energy stored in the magnetic field of the MR damper control coil.



Fig. 3. Time histories of voltage u_h and u_{DC}: f=5 Hz; a) A=2 V, b) A=6 V

It appears that within the time period from 0 to 10 ms, when no magnetic field energy is yet accumulated in the damper control coil, the currents i_h (Fig. 4a) and i_{DC} (Fig. 5a) in bridge B1 begin to flow when $|u_h|$ >0.7 V. At the instant the voltage u_h reaches zero, the direction of current ih flow is reversed (Figs. 4, 5) while maintaining its absolute value. Furthermore, immediately after the current flow i_h is reversed (when 0.1s<t<0.11s), its absolute value tends to decrease, despite an increase in absolute value of voltage u_h , which can be attributable to reduced current levels sustained in the circuit by the induction coil. The effect whereby the current flow is sustained by the damper control coil can be also observed for current inc whose minimal value at each time instant (t>0s) exceeds zero. Lower current levels, i_h and i_{DC} , in bridge B1 are associated with the voltage drop across diodes D1-D4. The smaller the voltage u_{DC} , the smaller the current i_{DC} . The average current level iDC for bridge B1 approaches 100 mA (when A=2 V, Fig. 5a). For the amplitude A=6V (Fig. 5b), it is approximately equal to 0.5 A. The average current level i_{DC} for bridge B2 is larger than the average current level registered for bridge B1, its respective values for the amplitudes A=2 V and A=6V being 200 mA and about 700 mA.



Fig. 4. Time histories of voltage u_h and current i_h: f=5 Hz; a) A=2 V, b) A=6 V



Fig. 5. Time histories of voltage uh and current iDC: f=5 Hz; a) A=2 V, b) A=6 V

Bridge efficiency is derived from the formula:

$$\eta = 100 \frac{\frac{1}{T} \int_{0}^{t} u_{DC}(t) i_{DC}(t) dt}{\frac{1}{T} \int_{0}^{t} u_{h}(t) i_{h}(t) dt} [\%]$$
(1)

Plots in Fig. 6 show the bridge efficiency for B1 and B2 in the frequency range (0.5, 10) Hz, for amplitudes A=2 V and A=6 V. Apparently, bridge efficiency is improved when the amplitude is 6 V. Actually, bridge efficiency η is improved at higher amplitude of voltage u_h because the voltage u_{DC} supplying the MR damper control coil is equal to voltage u_h less the constant voltage value $2xU_F$, being the sum of voltage drops across the bridge components. One has to bear in mind, however, that the actual values of voltage drops $2xU_F$ may differ depending on the type of applied system components (diodes or transistors) and depending on the current level i_{DC} in those elements, whilst they are independent of the input voltage level u_h . As regards bridge B1, its efficiency decreases with increasing frequency, whilst that of bridge B2 remains nearly constant, exceeding 95%.



Fig. 6. Efficiency vs frequency

4. PCB FABRICATION

The development of PCB design of an ideal bridge (B2) was supported by the KiCad software (KiCad EDA, 2020). All components comprising the SB, CB and RB blocks are implemented on a double-sided laminate. Most components have the surfacemounting device housings. In the double-sided placement configuration, the components are placed in the top and bottom layers, which allows the PCB dimensions to be minimised (57 mm x 53 mm) and placed on a two-layer copper board. The laminate thickness was 1.6 mm, and the base copper thickness equal to 35 µm. The copper plating was finished by the HASL (Hot Air Solder Levelling) method; thus, the conductive layer thickness was increased by several µm. The path width for signal routing and for supplying the SB and CB clock circuits is 0.2 mm. The paths to handle the block RB and other high current connectors were implemented using the copper planes of paths with the minimal width 0.5 mm. The vias implementing the electric connections between copper layers and effecting the heat release were 0.3 mm in diameter. The number of vias was increased to handle high-current connections. The view of top and bottom of the bridge PCB is shown in Figs. 7a, b.

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b) Fig. 7. Ideal rectifier bridge PCB: a) top view, b) bottom view

The PCB surface was subdivided into three respective areas to handle the SB, CB, RB blocks. The connections between regulator U1 and operational amplifiers U2 and U3 were designed to be as short as possible. The decoupling capacitors are placed close to the power supply terminal. Components that release heat (D1-D4 and Q1-Q4) in the RB block are separated from the SB and CB block circuits. The RB block connections were implemented on the power planes, which enabled the effective heat release to the PCB and to the ambience.

5. MEASUREMENT PROCEDURE

Bridge measurements were taken using the circuit shown schematically in Fig. 8, comprising a power amplifier (Sapiński et al. 2012) supplied from the power supply, voltage and current converters, an AD/DA board and a PC supporting the MATLAB/Simulink 2011b. The power amplifier generates alternating voltage fed to the bridge input (corresponding to uh) in accordance with the command signal u_{con} generated at the AD/DA board output. The amplifier is fitted with integrated converters, enabling the measurements of voltage u_h and current i_h in the harvester coil. Voltage u_{DC} and current i_{DC} in the MR damper control coil are measured with voltage-voltage (u/u) and current-voltage (i/u) converters based on op-amps AD629 and AD8622.

Registered quantities were duly converted into voltage signals in the range (+10, -10) V, sampled by the AD/DA board at the frequency 1 kHz.



Fig. 8. Diagram of the measurement system

Finally, B2 measurement data are benchmarked against the results obtained for bridge B1 (based on Schottky diodes). One has to bear in mind that the measurements of bridge B2 were taken using an external power supply (a Li-Ion 3.7 V battery) – JP1 in position 2. Plots in Figs. 9-11 show time histories of measured and simulated steady-state voltages u_{h} , u_{DC} and currents i_{h} , i_{DC} , Fig. 12 plots efficiency η in the function of frequency 1, derived from formula (1).



Fig. 9. Time histories of voltage uh and uDC: f=5 Hz; a) A=2 V, b) A=6 V

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Fig. 10. Time histories of voltage u_h and current i_h: f=5 Hz; a) A=2 V, b) A=6 V



Fig. 11. Time histories of voltage uh and current iDC; f=5 Hz; a) A=2 V, b) A=6 V



Fig. 12. Efficiency vs frequency

Measured voltages u_{DC} (Fig. 9) are in good agreement with the calculation results. Minor discrepancies are attributable to parasite resistances present in the bridge, not accounted for in the simulation model. The plots of i_h and i_{DC} (Figs 10-11) reveal certain discrepancies. The maximal difference between their instantaneous values for A=6 V is found to be 0.2 A and is registered for the two bridges under consideration. Actually, these discrepancies are the result of eddy currents induced in ferromagnetic material from which the MR damper rod is made. The simulation procedure used a solenoid coil where such phenomenon should not occur. The efficiency of bridge B2 obtained experimentally is 92%; for bridge B1, the respective values are: 50% (for A=2V) and 75% (for A=6V). These are found to be less than the simulated results, mostly due to the occurrence of eddy currents and parasite resistances (Fig. 12).

6. APPLICATION AND TESTING

To verify the adequacy of the newly developed bridge B2 design, experimental tests were conducted in the test rig (Sapiński et al. 2010) incorporating the harvester prototype, the RD-8040-1 damper, a passive spring rated at k=90,000 N/m attached to the sprung mass m=155 kg, a shaker and a measurement system (Fig. 13). The natural frequency of the SDOF system (sprung mass-damper-spring) is h=3.8 Hz. The measurement system comprised a PC, an AD/DA board supporting the MATLAB/Simulink, displacement sensors (S1, S2), force sensors (S3, S4), voltage-voltage (u/u) and current-voltage (i/u) converters. Measurements were taken of the shaker core displacement (input excitation) z, sprung mass displacement x, force F_{in} generated by the shaker, damper force F_d , voltage and current levels in the harvester coil u_h and i_h , voltage and current in the bridge u_{DC} and i_h (voltage and current in the MR damper control coil).

The excitations applied in the testing procedure were in the form of displacement inputs z with the amplitude 3.5 mm and frequency varied from 2 Hz to 10 Hz, with the step 0.1 Hz. Each test took 30 s and involved three phases: increasing the amplitude of applied inputs (0, 10) s, maintaining the amplitude of applied excitation on the fixed level (10, 20) s, decreasing the amplitude of excitation (20, 30) s. Figs. 14-16 plot the results of tests done in the time interval (10, 20) s, while the amplitude z of the applied excitation remained constant. Figs. 14-15 show the respective time histories of input voltage and current (u_h , i_h), output voltage and current (u_{DC} , i_{DC}) in bridges B1 and B2. Fig. 16 plots the displacement transmissibility coefficient T_{xz} in the function of frequency for the SDOF system governed by formula (2); expressing the efficiency of reduction of the sprung mass vibration amplitude.



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$$T_{xz} = \frac{\sqrt{\int_{0}^{T} x(t)^{2} dt}}{\sqrt{\int_{0}^{T} z(t)^{2} dt}}$$
(2)



Fig. 13. The test rig: a) schematic diagram, b) general view

It is readily apparent (see Fig. 14) that instantaneous values of voltage in the harvester u_h supplying bridge B1 are larger than the voltage supplying bridge B2 - it appears to be the consequence of a larger relative velocity of magnets motion with respect to the harvester coil, which is associated with a larger amplitude of sprung mass vibrations x (see Fig. 16). The maximal instantaneous values are about 2V (B1) and 1.4 V (B2). Despite lower values of voltage uh at the input to B2, currents *i_h* in the two bridges are similar in value, not exceeding 200 mA. Moreover, instantaneous values of voltage uDC at the bridge outputs are similar, too (Fig. 15), whilst the only registered discrepancy is the occurrence of negative values of instantaneous voltage, not exceeding -350 mV in the case of bridge B1. As regards bridge B2, no negative values of instantaneous voltage uDc are registered, and so, the instantaneous current intensity iDC in the damper control coil reaches a slightly higher value than in B1, which is indicative of lower loss of energy supplied to the control coil via bridge B2. In consideration of the voltage difference $|u_h - u_{DC}|$, it appears that the voltage drop across bridge B1 is 0.7 V, whilst for bridge B2, it becomes 20 mV. This observation is in good agreement with the results of simulations and measurements, outlined in Sections 4 and 5.

Transmissibility plots T_{xz} in Fig. 16 were derived under the following conditions of power supply to the MR damper control coil (the two-terminal network): no power supply (Case 1), direct supply from the harvester (Case 2), power supply from bridge B1 (Case 3), power supply from bridge B2 (Case 4). Apparently, the amplitude of sprung mass vibration in Case 4 is vastly reduced in the frequency

range (2, 6.5) Hz in relation to Case 2, whilst the maximal value of this coefficient registered at 3.9 Hz approaches T_{xz} =1.4. Its respective values registered in the remaining cases are: T_{xz} =3.4 at frequency 3.8 Hz (Case 1); T_{xz} =1.6 at frequency 4.1 Hz (Case 2); T_{xz} =1.7 at frequency 3.5 Hz (Case 3).



Fig. 14. Time histories of voltage u_h and current i_h ; f = 3.8 Hz







Fig. 16. Transmissibility coefficient T_{xz} vs frequency f

Obviously (see the plots in Fig. 1), the application of bridge B2 should be most favourable, as the amplitude of spring mass vibration is thus significantly reduced in the neighbourhood of resonance frequency, whilst the system's behaviour is less favourable at frequencies in excess of 5 Hz in Cases 2, 3, 4 when compared to Case 1.

This effect can be attributed to the presence of an MR damper dissipating energy placed in between the source of vibration (kin**\$** sciendo

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ematic input) and the sprung mass. In such system, when the excitation frequency should exceed $\sqrt{2}f_0$, the increase of damping in the system results in increased amplitudes of the sprung mass vibration. Its performance and condition will further deteriorate due to growing amounts of electric energy generated by a harvester at frequencies in excess of 5 Hz. In order to overcome this drawback, it is suggested that additional passive circuits converting the alternating current should be provided in between the harvester coil and the MR damper coil (Jastrzębski and Sapiński, 2017).

7. SUMMARY

The main aim of the study was to develop an ideal rectifier bridge to convert the AC signal to positive voltages in an MR damper-based regenerative vibration reduction system. Its structural design and fabrication techniques are outlined, and the results of simulations and laboratory testing are collated. The advantages of the newly developed bridge are demonstrated by benchmarking the measurement results against those obtained for a rectifier bridge based on Schottky diodes, leading us to the following conclusions.

- In the case of bridge B1, the power loss in the harvester coil MR damper control coil circuit is largely due to power loss in Schottky diodes (in the form of heat release). The actual level of power loss is associated with forward voltage and current flowing in diodes. In the case of bridge B2, the power loss is associated with the drain-source on-state resistance of transistors and current flowing through them. Compared to bridge B1, power loss is significantly reduced, and thus, the efficiency of the bridge (B2) can be improved.
- Efficiency of bridge B1 increases with increasing amplitude of voltage *u_h* and tends to decrease with increasing frequency.
 Efficiency of bridge B2 is constant; it is unrelated to the amplitude of voltage *u_h* and frequency *f*. For bridge B1, the efficiency becomes 75 %, for B2 92 %.
- Bridge B2 requires an external source of electric power to supply the electronic circuit for transistor control. This energy may be obtained by harvesting the energy of sprung mass vibration. In the case of no external supply, efficiency of bridge B1 is similar to that of bridge B2.
- In the vibration reduction system incorporating bridge B1, the value of transmissibility coefficient T_{xz} will not change by more than ±6% in relation to the case where the MR damper control coil is supplied directly from the harvester. Apparently, the amplitude of sprung mass vibration is decreased only in the frequency range (4, 7) Hz.
- Application of bridge B2 in the vibration reduction system results in a nearly 15% reduction of the sprung mass vibration amplitude in relation to the case where the bridge is absent. This effect is registered in the frequency range (2, 6.5) Hz.
- In each investigated case where the MR damper control coil is supplied from the harvester (with or without the bridge), at frequencies exceeding 5 Hz, the amplitude of sprung mass vibration tends to increase in relation to the case where the coil is not supplied. This is best revealed in systems incorporating bridge B2, and this drawback can be overcome through the application of passive electric circuits utilising the voltage resonance effect.

Further studies will investigate the electronic systems for conditioning of voltage generated by the harvester, and a dedicated high-efficiency DC/DC converter is planned to be developed.

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